

ARM926EJ-S S3C2450 Mango24R2 PIN MAP

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Crazy Embedded Laboratory

Document History

Revision	Date	Change note

1. PIN MAP	4
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1. PIN MAP

<Mango24R2 CPU Module>

CON1			
NO	PIN EXPLAIN	NO	PIN EXPLAIN
1	LCD_D0	2	CAM_DAT0
3	LCD_D1	4	CAM_DAT1
5	LCD_D2	6	CAM_DAT2
7	LCD_D3	8	CAM_DAT3
9	LCD_D4	10	CAM_DAT4
11	LCD_D5	12	CAM_DAT5
13	LCD_D6	14	CAM_DAT6
15	LCD_D7	16	CAM_DAT7
17	LCD_D8	18	CAM_RESET
19	GND	20	GND
21	LCD_D9	22	CAM_CLKOUT
23	LCD_D10	24	CAM_PCLK
25	LCD_D11	26	CAM_HSYNC
27	LCD_D12	28	CAM_VSYNC
29	VDD_3v3	30	VDD_3v3
31	LCD_D13	32	SPIMISO0
33	LCD_D14	34	SPIMOSI0
35	LCD_D15	36	SPICLK0
37	LCD_D16	38	SPIMISO1
39	LCD_D17	40	SPIMOSI1
41	LCD_D18	42	SPICLK1
43	LCD_D19	44	SS0
45	LCD_D20	46	SS1
47	LCD_D21	48	GPB9
49	VDD_3v3	50	VDD_3v3

51	LCD_D22	52	EINT16
53	LCD_D23	54	EINT17
55	LCD_LEND	56	EINT18
57	LCD_VCLK	58	GPB8
59	GND	60	GND
61	LCD_HSYNC	62	CPU_RTCK
63	LCD_VSYNC	64	GPC5
65	LCD_DE	66	GPC6
67	UARTCLK	68	GPC7
69	TXD1	70	TXD0
71	RXD1	72	RXD0
73	nCTS1	74	nCTS0
75	nRTS1	76	nRTS0
77	TXD3	78	RXD2
79	RXD3	80	TXD2

CON2			
NO	PIN EXPLAIN	NO	PIN EXPLAIN
1	EBI_A0	2	EBI_D0
3	EBI_A1	4	EBI_D1
5	EBI_A2	6	EBI_D2
7	EBI_A3	8	EBI_D3
9	EBI_A4	10	EBI_D4
11	EBI_A5	12	EBI_D5
13	EBI_A6	14	EBI_D6
15	EBI_A7	16	EBI_D7
17	EBI_A8	18	EBI_D8
19	GND	20	GND
21	EBI_A9	22	EBI_D9

23	EBI_A10	24	EBI_D10
25	EBI_A11	26	EBI_D11
27	EBI_A12	28	EBI_D12
29	EBI_A13	30	EBI_D13
31	EBI_A14	32	EBI_D14
33	EBI_A15	34	EBI_D15
35	EBI_A16	36	EBI_OEn
37	EBI_A17	38	EBI_WEn
39	VDD_3v3	40	VDD_3v3
41	VDD_3v3	42	VDD_3v3
43	EBI_A18	44	EBI_WAITn
45	EBI_A19	46	EBI_nRBE1
47	EBI_A20	48	EBI_nRBE0
49	EBI_A21	50	I2S_LRCK
51	EBI_A22	52	I2S_SCLK
53	EBI_A23	54	I2S_CDCLK
55	EBI_A24	56	I2S_DIN
57	EBI_A25	58	I2S_DOUT
59	GND	60	GND
61	EBI_CSn0	62	TCLK
63	EBI_CSn1	64	CLKOUT0
65	EBI_CSn2	66	CLKOUT1
67	EBI_CSn3	68	CF_nOE
69	EBI_CSn4	70	CF_new
71	EBI_CSn5	72	CF_nIREQ
73	TOUT0	74	CF_nINPACK
75	TOUT1	76	CF_nREG
77	TOUT2	78	CF_RESET
79	TOUT3	80	CF_CardPWR

CON3			
NO	PIN EXPLAIN	NO	PIN EXPLAIN
1	OM0	2	EINT0
3	OM1	4	EINT1
5	OM2	6	EINT2
7	OM3	8	EINT3
9	OM4	10	EINT4
11	CPU_nRST	12	EINT5
13	nRSTOUT	14	EINT6
15	nBATT_FLT	16	EINT7
17	PWREN	18	EINT8
19	GND	20	GND
21	TOUCH_YM	22	EINT9
23	TOUCH_YP	24	EINT10
25	TOUCH_XM	26	EINT11
27	TOUCH_XP	28	EINT12
29	VDD_3v3	30	VDD_3v3
31	EXTCLK	32	EINT13
33	I2C_SCL	34	VDD_CAM
35	I2C_SDA	36	EINT14
37	AIN0	38	VDD_RTC
39	AIN1	40	EINT15
41	AIN2	42	CPU_TMS
43	AIN3	44	CPU_TCK
45	AIN4	46	CPU_TDI
47	AIN5	48	CPU_TDO
49	VDD_3v3	50	VDD_3v3
51	VREF	52	CPU_nTRST
53	SD0_nWP	54	GND

55	SD0_nCD	56	USBC_DP
57	SD0_LED	58	USBC_DP
59	GND	60	GND
61	SD0_CLK	62	USBH_DP
63	SD0_CMD	64	USBH_DN
65	SD0_DAT0	66	GND
67	SD0_DAT1	68	GPB10
69	SD0_DAT2	70	SD1_CLK
71	SD0_DAT3	72	SD1_CMD
73	SD0_DAT4	74	SD1_DAT0
75	SD0_DAT5	76	SD1_DAT1
77	SD0_DAT6	78	SD1_DAT2
79	SD0_DAT7	80	SD1_DAT3

<Mango24R2 Base Board PINMAP>

CON19			
NO	PIN EXPLAIN	NO	PIN EXPLAIN
1	VDD_3v3	2	VDD_3v3
3	EBI_D0	4	EBI_A1
5	EBI_D1	6	EBI_A2
7	EBI_D2	8	EBI_A3
9	EBI_D3	10	EBI_A4
11	EBI_D4	12	EBI_A5
13	EBI_D5	14	EBI_A6
15	EBI_D6	16	EBI_A7
17	EBI_D7	18	EBI_A8
19	EBI_D8	20	EBI_A9
21	EBI_D9	22	EBI_A10

23	EBI_D10	24	EBI_A11
25	EBI_D11	26	EBI_A12
27	EBI_D12	28	EBI_A13
29	EBI_D13	30	EBI_A14
31	EBI_D14	32	EBI_A15
33	EBI_D15	34	EBI_OEn
35	EBI_CSn0	36	EBI_WEn
37	EBI_CSn4	38	EBI_WAITn
39	GND	40	GND

CON20			
NO	PIN EXPLAIN	NO	PIN EXPLAIN
1	VDD_3v3	2	VDD_3v3
3	TXD2	4	EINT0
5	RXD2	6	EINT1
7	TXD3	8	EINT2
9	RXD3	10	EINT3
11	TOUT0	12	EINT4
13	TOUT1	14	EINT5
15	SPIMISO0	16	EINT6
17	SPIMOSIO	18	EINT7
19	SPICLK0	20	EINT8
21	SS0	22	EINT9
23	AIN0	24	EINT10
25	AIN1	26	EINT11
27	VREF	28	EINT12
29	GPC7	30	EINT13
31	GPC6	32	EINT14
33	GPC5	34	EINT15

35	5V_IN	36	EINT16
37	5V_IN	38	EINT17
39	GND	40	GND